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1 [Signal integrity optimization on the pad assignment for high-speed VLSI design](#)

Kai-Yuan Chao, D. F. Wong

December 1995 ICCAD '95: Proceedings of the 1995 IEEE/ACM international conference on Computer-aided design

Publisher: IEEE Computer Society

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In this paper, an efficient method is proposed to effectively minimize both simultaneous switching noise and crosstalk that are inevitably caused by package inductance and capacitance during the design of high-speed/high-bandwidth circuits. Due to its ...

Keywords: pad assignment, signal integrity, crosstalk, simultaneous switching Noise, floorplanning, packaging

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2 [A metric for analyzing effective on-chip inductive coupling](#)

Guoan Zhong, Cheng-Kok Koh, Kaushik Roy

January 2003 ASPDAC: Proceedings of the 2003 conference on Asia South Pacific design automation

Publisher: ACM

Full text available: pdf(98.62 KB) Additional Information: [full citation](#), [abstract](#), [references](#)

In this paper, we propose a metric for effective inductive coupling: the matrix $(R + j\omega L)^{-1}$, where R and L are the resistance and inductance matrices. We use this metric to analyze the effectiveness of shields on ...

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www.dandeanpro.com

3 [Real-time integrity constraints](#)

Jan Chomicki

July 1992 PODS '92: Proceedings of the eleventh ACM SIGACT-SIGMOD-SIGART symposium on Principles of database systems

Publisher: ACM

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Full text available:  [pdf\(811.63 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)


We propose that Past Metric Temporal Logic (Temporal Logic with real-time operators referring to the past) be used as a language for specifying real-time integrity constraints. Building on our earlier work, we develop efficient, history-less methods ...

4 [The similarity metric](#)

Ming Li, Xin Chen, Xin Li, Bin Ma, Paul Vitányi

January 2003 SODA '03: Proceedings of the fourteenth annual ACM-SIAM symposium on Discrete algorithms

Publisher: Society for Industrial and Applied Mathematics

Full text available:  [pdf\(1.04 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

A new class of metrics appropriate for measuring effective similarity relations between sequences, say one type of similarity per metric, is studied. We propose a new "normalized information distance", based on the noncomputable notion of Kolmogorov ...


5 [Spatial data integrity constraints in object oriented geographic data modeling](#)



Karla A. V. Borges, Alberto H. F. Laender, Clodoveu A. Davis, Jr.

November 1999 GIS '99: Proceedings of the 7th ACM international symposium on Advances in geographic information systems

Publisher: ACM

Full text available:  [pdf\(80.31 KB\)](#) Additional Information: [full citation](#), [references](#), [cited by](#), [index terms](#)

Keywords: conceptual modeling, geographic data modeling


6 [Built-in self-test for signal integrity](#)



Mehrdad Nourani, Amir Attarha

June 2001 DAC '01: Proceedings of the 38th conference on Design automation

Publisher: ACM

Full text available:  [pdf\(149.79 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Unacceptable loss of signal integrity may harm the functionality of SoCs permanently or intermittently. We propose a systematic approach to model and test signal integrity in deep-submicron high-speed interconnects. Various signal integrity problems ...

7 [Active integrity constraints](#)




Sergio Flesca, Sergio Greco, Ester Zuppano

August 2004 PPDP '04: Proceedings of the 6th ACM SIGPLAN international

conference on Principles and practice of declarative programming

Publisher: ACM

Full text available:  [pdf\(237.89 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

In this paper we deal with inconsistent databases and propose a logic framework that allows specifying sets of actions which should be performed to make databases consistent (repairs). The motivation of this work stems from the observation that in repairing ...

Keywords: inconsistent database, integrity constraints, logic programming


8 [Signal integrity fault analysis using reduced-order modeling](#)



Amir Attarha, Mehrdad Nourani

June 2002 DAC '02: Proceedings of the 39th conference on Design automation

Publisher: ACM

Full text available:  [pdf\(83.57 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

This paper aims at analysis of signal integrity for the purpose of testing high speed interconnects. This requires taking into account the effect of inputs as well as parasitic RLC elements of the interconnect. To improve the analysis/simulation time ...

Keywords: integrity fault, locality factor, reduced-order model, signal integrity, test pattern generation

9 [The Hamming metric in genetic algorithms and its application to two network problems](#)




William G. Frederick, Robert L. Sedlmeyer, Curt M. White

March 1993 SAC '93: Proceedings of the 1993 ACM/SIGAPP symposium on

Applied computing: states of the art and practice

Publisher: ACM

Full text available:  [pdf\(382.55 KB\)](#) Additional Information: [full citation](#), [references](#), [cited by](#), [index terms](#)

10 [Signal integrity management in an SoC physical design flow](#)



Murat Becer, Ravi Vaidyanathan, Chanhee Oh, Rajendran Panda

April 2003 ISPD '03: Proceedings of the 2003 international symposium on

Physical design

Publisher: ACM


Full text available:  [pdf\(163.22 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Signal integrity closure is one of the key challenges in DSM (Deep-SubMicron) physical design. In this paper, we propose a physical design methodology which includes signal integrity management through noise analysis and repair at multiple phases of ...

Keywords: crosstalk noise, noise avoidance, noise repair, signal integrity

11 [Efficient checking of temporal integrity constraints using bounded history encoding](#)

-  Jan Chomicki
June 1995 ACM Transactions on Database Systems (TODS). Volume 20 Issue 2
Publisher: ACM

Full text available:  pdf(2.70 MB) Additional Information: full citation, abstract, references, cited by, index terms, review

We present an efficient implementation method for temporal integrity constraints formulated in Past Temporal Logic. Although the constraints can refer to past states of the database, their checking does not require that the entire database history be ...

Keywords: active databases, database integrity, integrity constraints, real-time databases, temporal databases, temporal logic, triggers


12 Area efficient architectures for information integrity in cache memories

-  Seongwoo Kim, Arun K. Somani
May 1999 ACM SIGARCH Computer Architecture News. Volume 27 Issue 2
Publisher: ACM

Full text available:  pdf(227.09 KB)  Publisher Site Additional Information: full citation, abstract, references, cited by, index terms

Information integrity in cache memories is a fundamental requirement for dependable computing. Conventional architectures for enhancing cache reliability using check codes make it difficult to trade between the level of data integrity and the chip area ...

13 Reputation-based framework for high integrity sensor networks

-  Saurabh Ganeriwal, Mani B. Srivastava
October 2004 SASN '04: Proceedings of the 2nd ACM workshop on Security of ad hoc and sensor networks
Publisher: ACM

Full text available:  pdf(469.08 KB) Additional Information: full citation, abstract, references, cited by, index terms

The traditional approach of providing network security has been to borrow tools from cryptography and authentication. However, we argue that the conventional view of security based on cryptography alone is not sufficient for the unique characteristics ...

Keywords: bayesian formulation, cryptography, framework, reputation, security, sensor networks, trust

14 A Novel Metric for Interconnect Architecture Performance

- Parthasarathi Dasgupta, Andrew B. Kahng, Swamy Muddu
March 2003 DATE '03: Proceedings of the conference on Design, Automation and Test in Europe - Volume 1. Volume 1
Publisher: IEEE Computer Society

Full text available:  pdf(194.68 KB)  Publisher Site Additional Information: full citation, abstract, references, cited by, index terms

We propose a new metric for evaluation of interconnect architectures. This metric is computed by optimal assignment of wires from a given wire length distribution (WLD) to a given interconnect architecture (IA). This new metric, the rank of an IA, is ...


15 Area efficient architectures for information integrity in cache memories

Seongwoo Kim, Arun K. Somani

May 1999 ISCA '99: Proceedings of the 26th annual international symposium on Computer architecture

Publisher: IEEE Computer Society

Additional Information: [full citation](#),

Full text available:  [pdf\(227.09 KB\)](#)  [Publisher Site](#)

[abstract](#),
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Information integrity in cache memories is a fundamental requirement for dependable computing. Conventional architectures for enhancing cache reliability using check codes make it difficult to trade between the level of data integrity and the chip area ...

16 Counteracting Oracle attacks

Ilaria Venturini

September 2004 MM&Sec '04: Proceedings of the 2004 workshop on Multimedia and security

Publisher: ACM

Full text available:  [pdf\(138.29 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#),
[cited by](#), [index terms](#)

In the paper, we address oracle attacks for integrity watermarking in a communication setting. We show how an integrity verification algorithm can be modified in order to counteract an oracle attack. As an alternative to set up suitable communication ...


Keywords: covert channel, integrity watermarking, oracle attack, security, semi-fragile watermarking

17 Secure program partitioning

Steve Zdancewic, Lantian Zheng, Nathaniel Nystrom, Andrew C. Myers

August 2002 ACM Transactions on Computer Systems (TOCS). Volume 20 Issue 3

Publisher: ACM

Full text available:  [pdf\(497.12 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#),
[cited by](#), [index terms](#)

This paper presents secure program partitioning, a language-based technique for protecting confidential data during computation in distributed systems containing mutually untrusted hosts. Confidentiality and integrity policies can be expressed by annotating ...


Keywords: Confidentiality, declassification, distributed systems, downgrading, integrity, mutual distrust, secrecy, security policies, type systems

18 [Simultaneous shield insertion and net ordering for capacitive and inductive coupling minimization](#)

Kevin M. Lepak, Min Xu, Jun Chen, Lei He

July 2004 ACM Transactions on Design Automation of Electronic Systems (TODAES). Volume 9 Issue 3

Publisher: ACM

Full text available:  pdf(308.22 KB) Additional Information: full citation, abstract, references, cited by, index terms

In this article, we first show that existing net ordering formulations to minimize noise are no longer sufficient with the presence of inductive noise, and shield insertion is needed to minimize inductive noise. Using a K_{eff} ...


Keywords: VLSI physical design automation, and on-chip inductance, net ordering, noise minimization, shielding, signal integrity

19 [Static noise analysis with noise windows](#)

Ken Tseng, Vinod Kariat

June 2003 DAC '03: Proceedings of the 40th conference on Design automation

Publisher: ACM

Full text available:  pdf(122.54 KB) Additional Information: full citation, abstract, references, cited by, index terms

As processing technology scales down to the nanometer regime, capacitive crosstalk is having an increasingly adverse effect on circuit functionality, leading to increasing number of chip failures. In this paper, we propose mapping the static crosstalk ...


Keywords: crosstalk, noise, signal integrity

20 [Protected transmission of biometric user authentication data for oncard-matching](#)

Ulrich Waldmann, Dirk Scheuermann, Claudia Eckert

March 2004 SAC '04: Proceedings of the 2004 ACM symposium on Applied computing


Publisher: ACM

Full text available:  pdf(574.45 KB) Additional Information: full citation, abstract, references, cited by

Since fingerprint data are no secrets but of public nature, the verification data transmitted to a smartcard for oncard-matching need protection by appropriate means in order to assure data origin in the biometric sensor and to prevent bypassing the ...

Keywords: authentication, biometrics, cryptographic protocols, data integrity, electronic signature, oncard-matching, smartcards, system security, tamper proof environment

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